

#### 200V PNP HIGH VOLTAGE TRANSISTOR PowerDI<sup>®</sup>5

#### **Features**

- 43% smaller than SOT223; 60% smaller than TO252
- Maximum height just 1.1mm
- Rated up to 3.2W
- V<sub>CEO</sub> = -200V
- I<sub>C</sub> = -2A; I<sub>CM</sub> = -5A
- Low Saturation voltage
- Lead, Halogen, and Antimony Free/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

### **Application**

DC – DC conversion

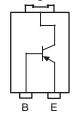
### **Mechanical Data**

- Case: PowerDI<sup>®</sup>5
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe.
  Solderable per MIL-STD-202, Method 208 ®
- Weight: 0.093 grams (approximate)









Top View

Bottom View

Device Schematic

Pin-out diagram

### Ordering Information (Note 3)

Part Number	Case	Packaging
DXTP03200BP5-13	PowerDI <sup>®</sup> 5	5000/Tape & Reel

Notes:

- 1. No purposefully added lead. Halogen and Antimony Free.
- 2. Diodes Inc's "Green" Policy can be found on our website at http://www.diodes.com
- 3. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

## **Marking Information**







# **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-220	V
Collector-Emitter Voltage	$V_{CEO}$	-200	V
Emitter-Base Voltage	$V_{EBO}$	-7	V
Continuous Collector Current	Ic	-2	А
Base Current	Ι <sub>Β</sub>	-1	A
Peak Pulse Current	Ісм	-5	A

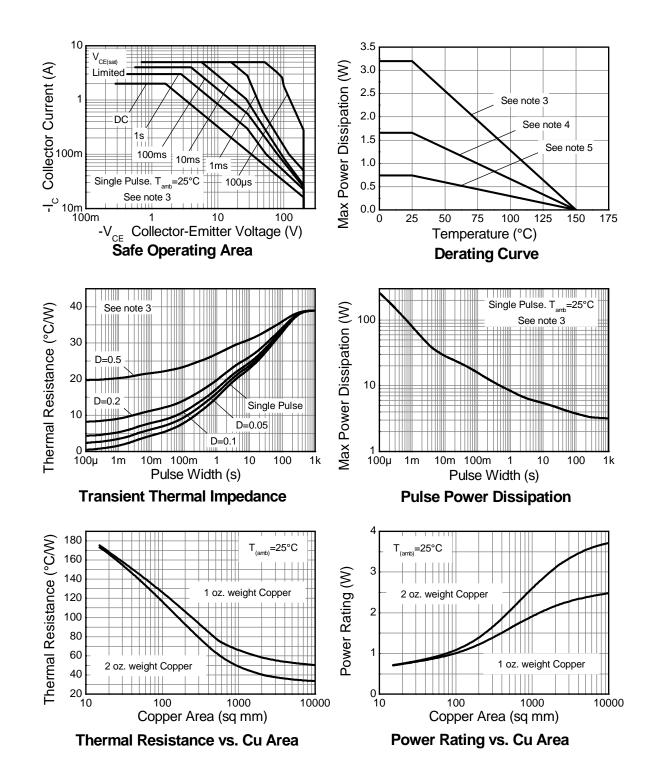
### Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation @ T <sub>A</sub> = 25°C (Note 4)	$P_{D}$	3.2	W
Thermal Resistance, Junction to Ambient Air (Note 4) @T <sub>A</sub> = 25°C	$R_{ hetaJA}$	39	°C/W
Power Dissipation @ T <sub>A</sub> = 25°C (Note 5)	$P_{D}$	1.7	W
Thermal Resistance, Junction to Ambient Air (Note 5) @T <sub>A</sub> = 25°C	$R_{ hetaJA}$	75	°C/W
Power Dissipation @ T <sub>A</sub> = 25°C (Note 6)	$P_{D}$	0.74	W
Thermal Resistance, Junction to Ambient Air (Note 6) @T <sub>A</sub> = 25°C	$R_{ hetaJA}$	169	°C/W
Thermal Resistance, Junction to Collector Terminal	$R_{ heta JT}$	5.6	°C/W
Operating and Storage Temperature Range	$T_J, T_STG$	-55 to +150	°C

Notes:

- Device mounted on FR-4 PCB, single sided 2 oz. copper, collector pad dimensions 25mm x 25mm.
  Device mounted on FR-4 PCB, single sided 1 oz. copper, collector pad dimensions 25mm x 25mm.
  Device mounted on FR-4 PCB, single sided 1 oz. copper, minimum recommended pad layout.









## **Electrical Characteristics** @TA = 25°C unless otherwise specified

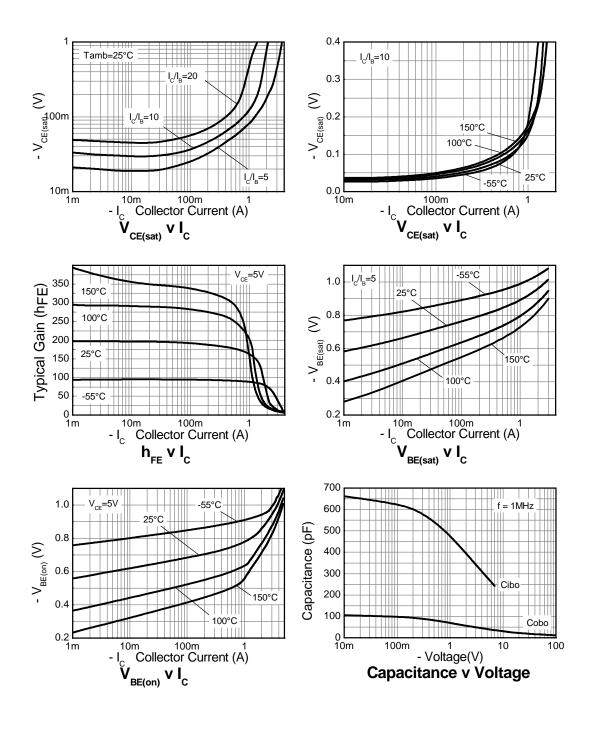
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	-220	-245	-	V	$I_C = -100 \mu A$
Collector-Emitter Breakdown Voltage (Note 7)	V <sub>(BR)CEO</sub>	-200	-225	ı	<b>V</b>	$I_C = -10 \text{mA}$
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	-7	-8.4	ı	<b>V</b>	$I_E = -100 \mu A$
Collector Cutoff Current	I <sub>CBO</sub>	-	<1 -	-50 -0.5	nA μA	V <sub>CB</sub> = -200V V <sub>CB</sub> = -200V, T <sub>A</sub> = 100 °C
Emitter Cutoff Current	I <sub>EBO</sub>	-	<1	-10	nA	$V_{EB} = -6V$
Collector-Emitter Saturation Voltage (Note 7)	V <sub>CE(sat)</sub>	- - -	-37 -130 -135 -180	-50 -155 -160 -275	mV	I <sub>C</sub> = -0.1A, I <sub>B</sub> = -10mA I <sub>C</sub> = -0.5A, I <sub>B</sub> = -25mA I <sub>C</sub> = -1A, I <sub>B</sub> = -100mA I <sub>C</sub> = -2A, I <sub>B</sub> = -400mA
Base-Emitter Saturation Voltage (Note 7)	V <sub>BE(sat)</sub>	_	-955	-1100	mV	$I_C = -2A$ , $I_B = -400mA$
Base-Emitter Turn-On Voltage (Note 7)	V <sub>BE(on)</sub>	_	-860	-1000	mV	$V_{CE} = -5V, I_{C} = -2A$
DC Current Gain (Note 7)	h <sub>FE</sub>	100 100 20	195 170 50 5	- 300 - -		V <sub>CE</sub> = -5V, I <sub>C</sub> = -10mA V <sub>CE</sub> = -5V, I <sub>C</sub> = -1A V <sub>CE</sub> = -5V, I <sub>C</sub> = -2A V <sub>CE</sub> = -5V, I <sub>C</sub> = -5A
Transition Frequency	f <sub>T</sub>	-	105	-	MHz	$V_{CE} = -10V, I_{C} = -100mA,$ f = 50MHz
Output Capacitance	$C_{obo}$	-	31	-	рF	$V_{CB} = -10V$ , $f = 1MHz$
Delay Time	t <sub>d</sub>	-	21	Ī	ns	
Rise Time	t <sub>r</sub>	=	18	=	ns	$V_{CC} = -50V, I_{C} = -1A,$
Storage Time	ts	-	680	-	ns	$I_{B1} = -I_{B2} = -100 \text{mA}$
Fall Time	t <sub>f</sub>	-	75	-	ns	

Notes: 7. Pulse Test: Pulse width  $\leq 300 \mu s$ . Duty cycle  $\leq 2.0\%$ .

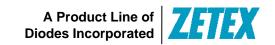




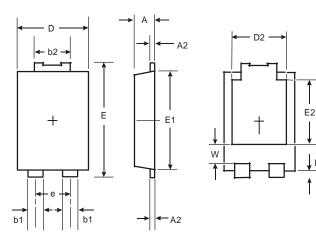
## **Typical Characteristic**





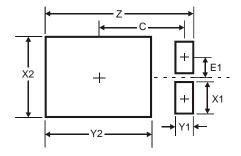


# **Package Outline Dimensions**



PowerDI <sup>®</sup> 5			
Dim	Min	Max	
Α	1.05	1.15	
A2	0.33	0.43	
b1	0.80	0.99	
b2	1.70	1.88	
D	3.90	4.05	
D2	3.054 Typ		
Е	6.40	6.60	
e	1.84 Typ		
E1	5.30	5.45	
E2	3.549 Typ		
١	0.75	0.95	
L1	0.50	0.65	
W	1.10	1.41	
All Dimensions in mm			

## **Suggested Pad Layout**



Dimensions	Value (in mm)
Z	6.6
X1	1.4
X2	3.6
Y1	0.8
Y2	4.7
С	3.87
E1	0.0





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